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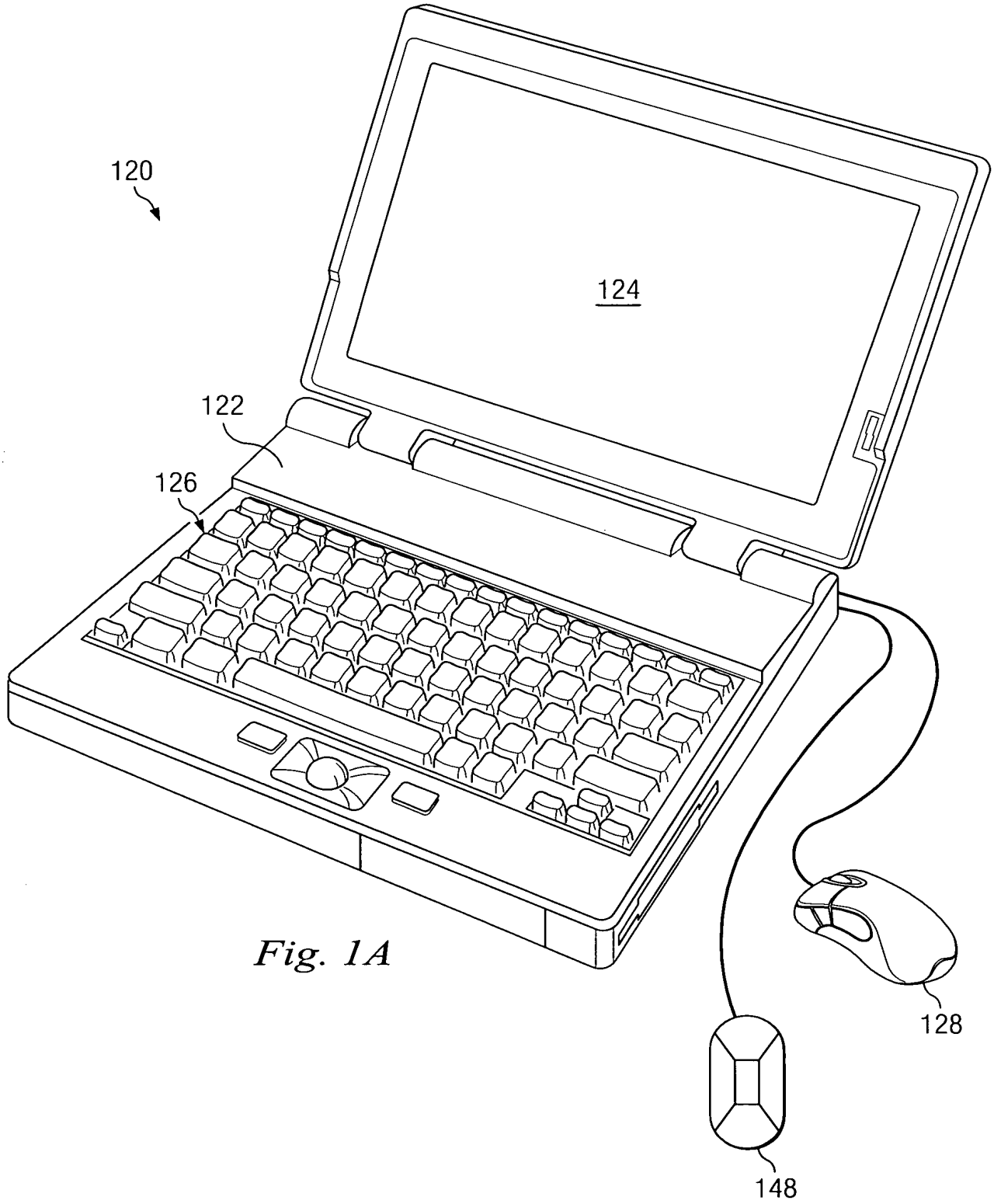

Fig. 1A

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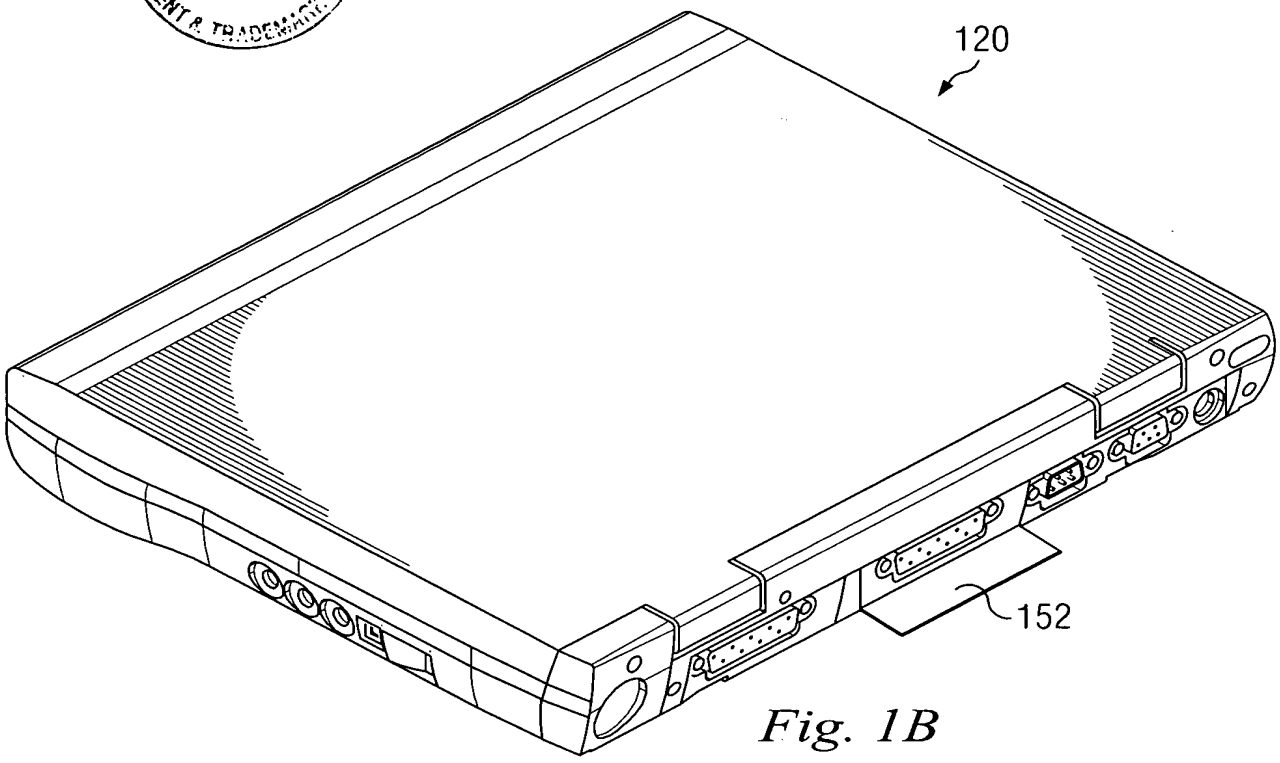

Fig. 1B

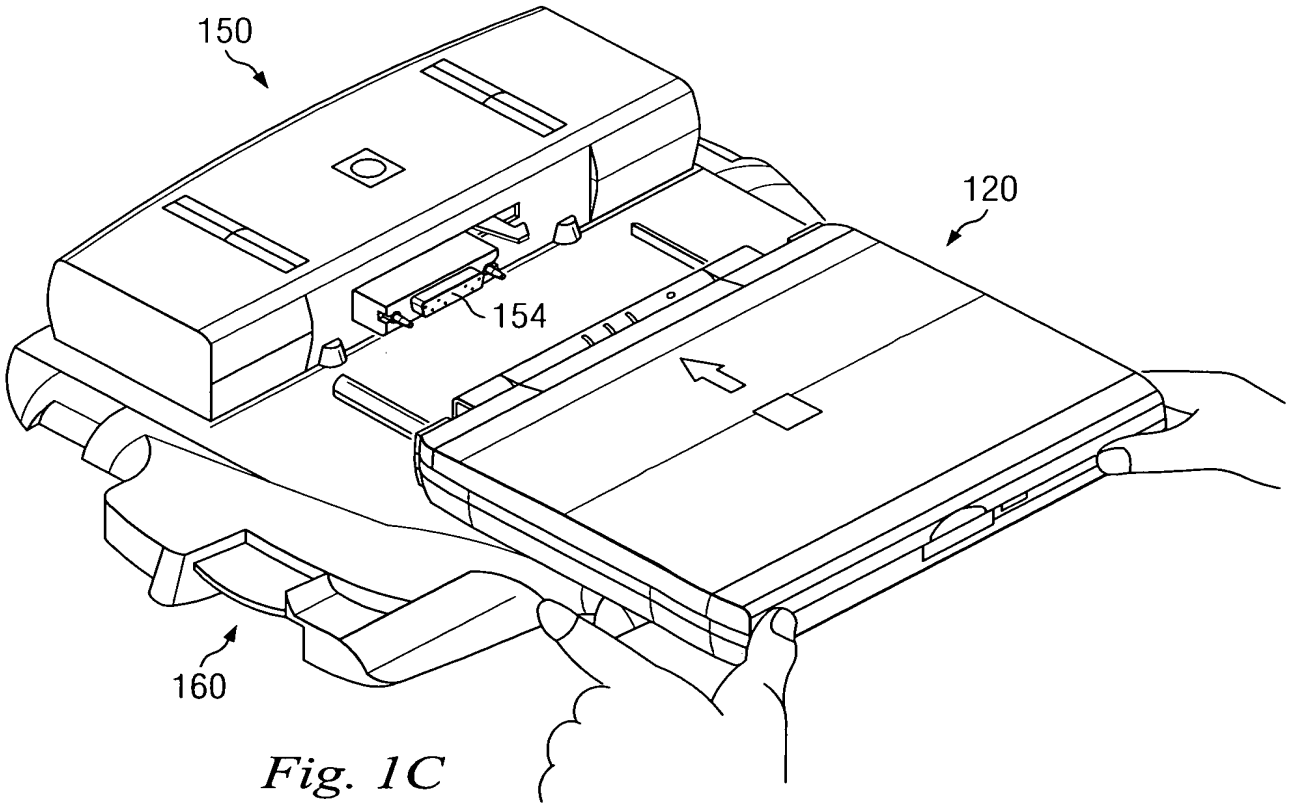

Fig. 1C

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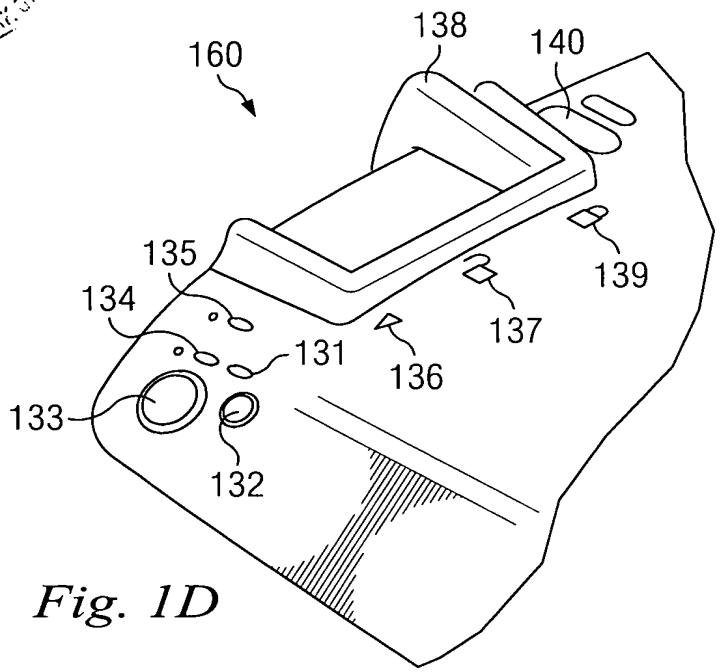

Fig. 1D

DOCKING DEVICE CLASS CIRCUIT

THE DOCKING DEVICE CLASS CIRCUIT IS A DEVICE (1) WHICH WILL INTERFACE AND FUNCTION WITH A BUS FOR WHICH WELL KNOWN STANDARDS (EITHER DE FACTO OR DE JURE) ARE DEFINED (e.g., USB, PCI, AT, ETC.), AND (2) WHICH CONTAINS WITHIN IT INFORMATION WHICH DEFINES THE AT LEAST ONE SUBSET OF THE "UNIVERSE" OF A DOCKING STATION OF WHICH IT (THE DOCKING DEVICE CLASS CIRCUIT) FORMS A PART, (3) WHICH WILL THEREBY ALLOW IDENTIFICATION AND/OR CONTROL OF THE ONE OR MORE DATA PROCESSING SYSTEM COMPONENTS WHICH MAKE UP AT LEAST ONE SUBSET OF THE UNIVERSE OF THE DOCKING STATION VIA USE OF WELL KNOWN AND WELL-DEFINED FUNCTIONS ASSOCIATED WITH THE BUS WITH WHICH THE DOCKING DEVICE CLASS CIRCUIT IS INTERFACED. THE "UNIVERSE" OF THE DOCKING STATION CAN INCLUDE AD HOC HARDWARE AND SOFTWARE WITHIN THE DOCKING STATION (e.g., A DOCKING-STATION HARD-DRIVE ACCESS LED AND SWITCH/UNDOCK BUTTON), THE BUS STRUCTURES AND ASSOCIATED DEVICES IN THE DOCKING STATION, AND/OR THE POWER SUPPLY TYPES AND/OR VOLTAGES WITHIN THE DOCKING STATION

700

702 — BUS OF KNOWN TYPE, FOR WHICH WELL-DEFINED STANDARDS EXIST. FOR EXAMPLE, USB, PCI, AT, ETC

154 — DOCKING INTERFACE CONNECTOR

Fig. 7

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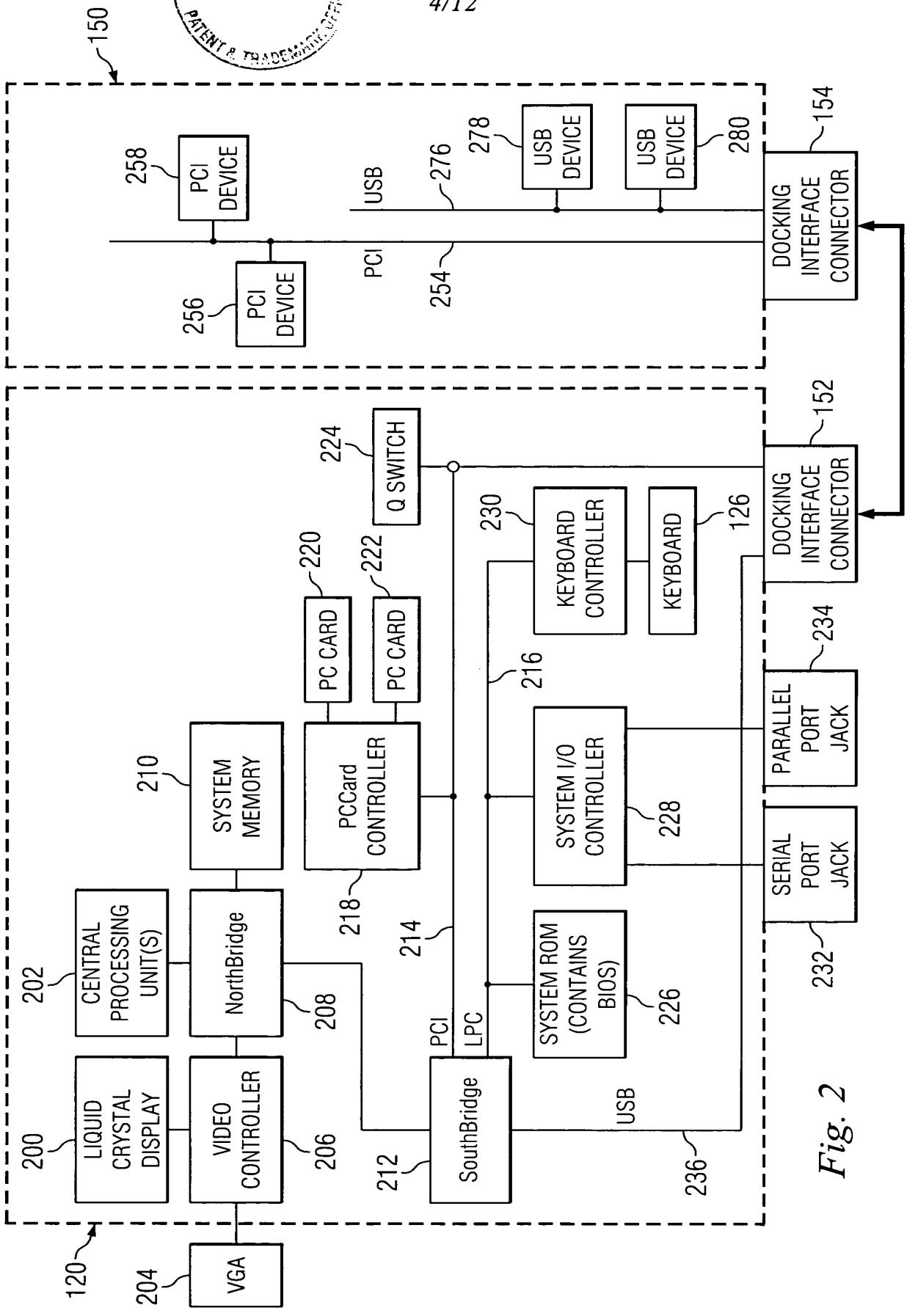

Fig. 2

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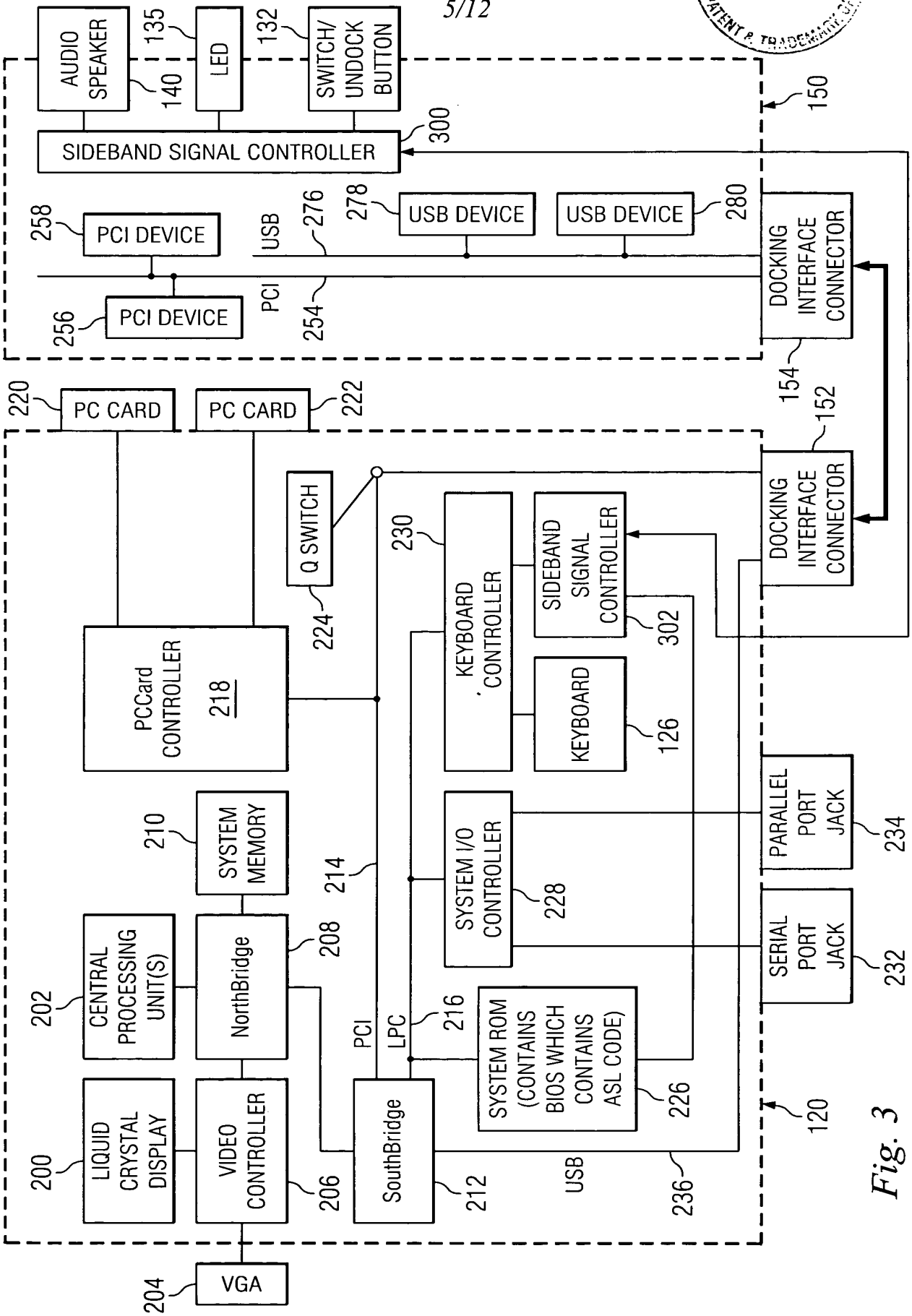

Fig. 3

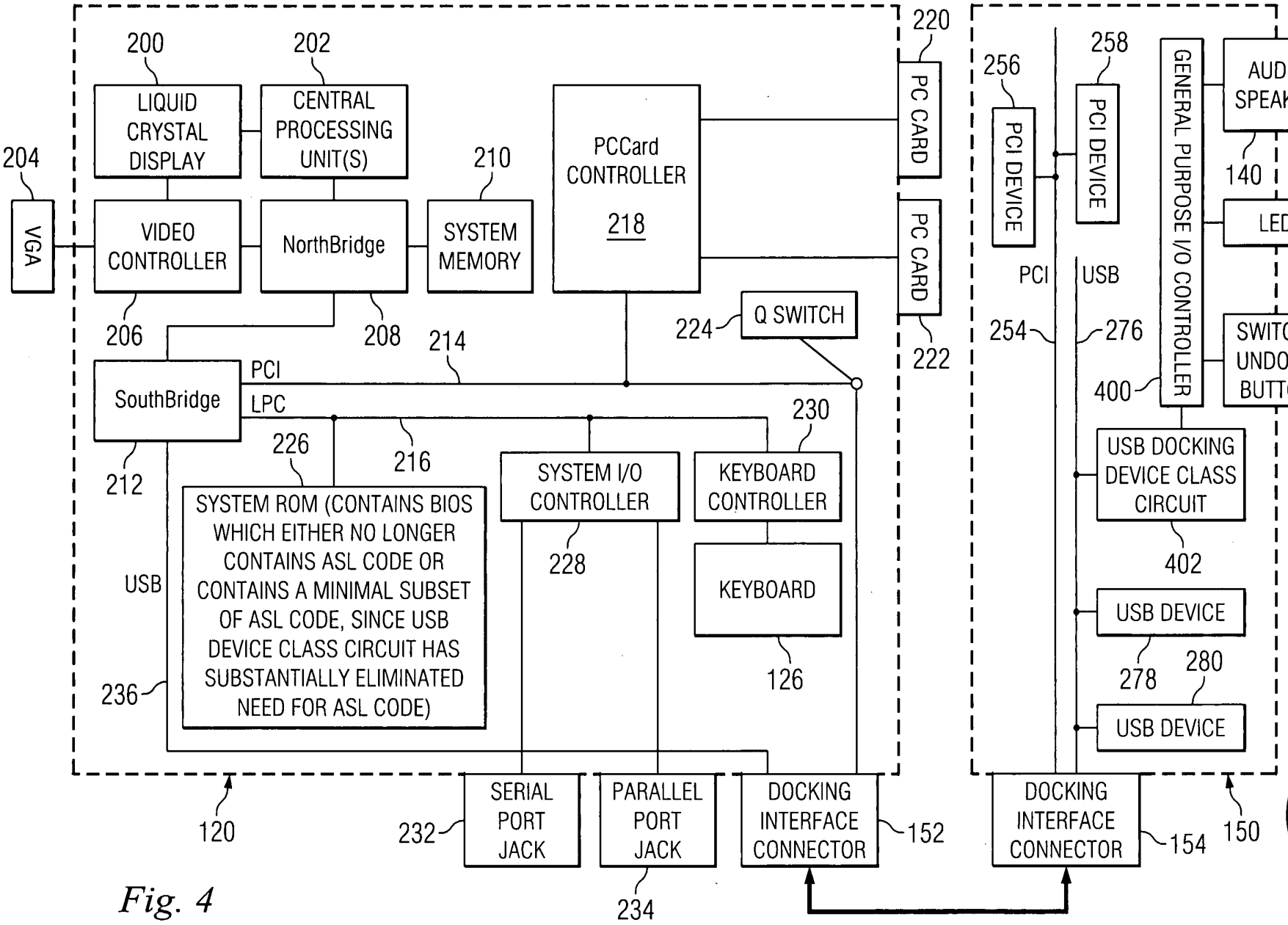

Fig. 4

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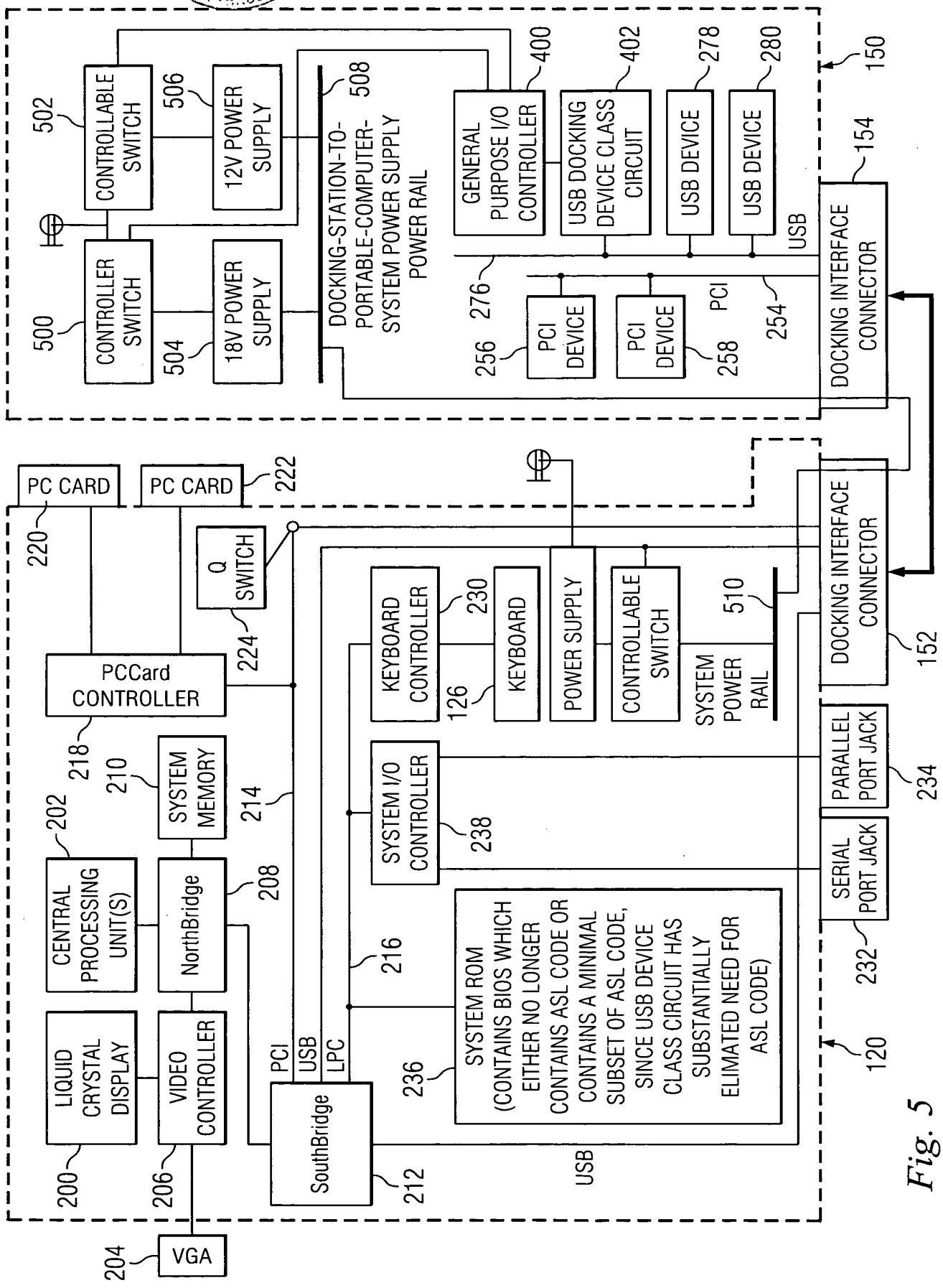

Fig. 5

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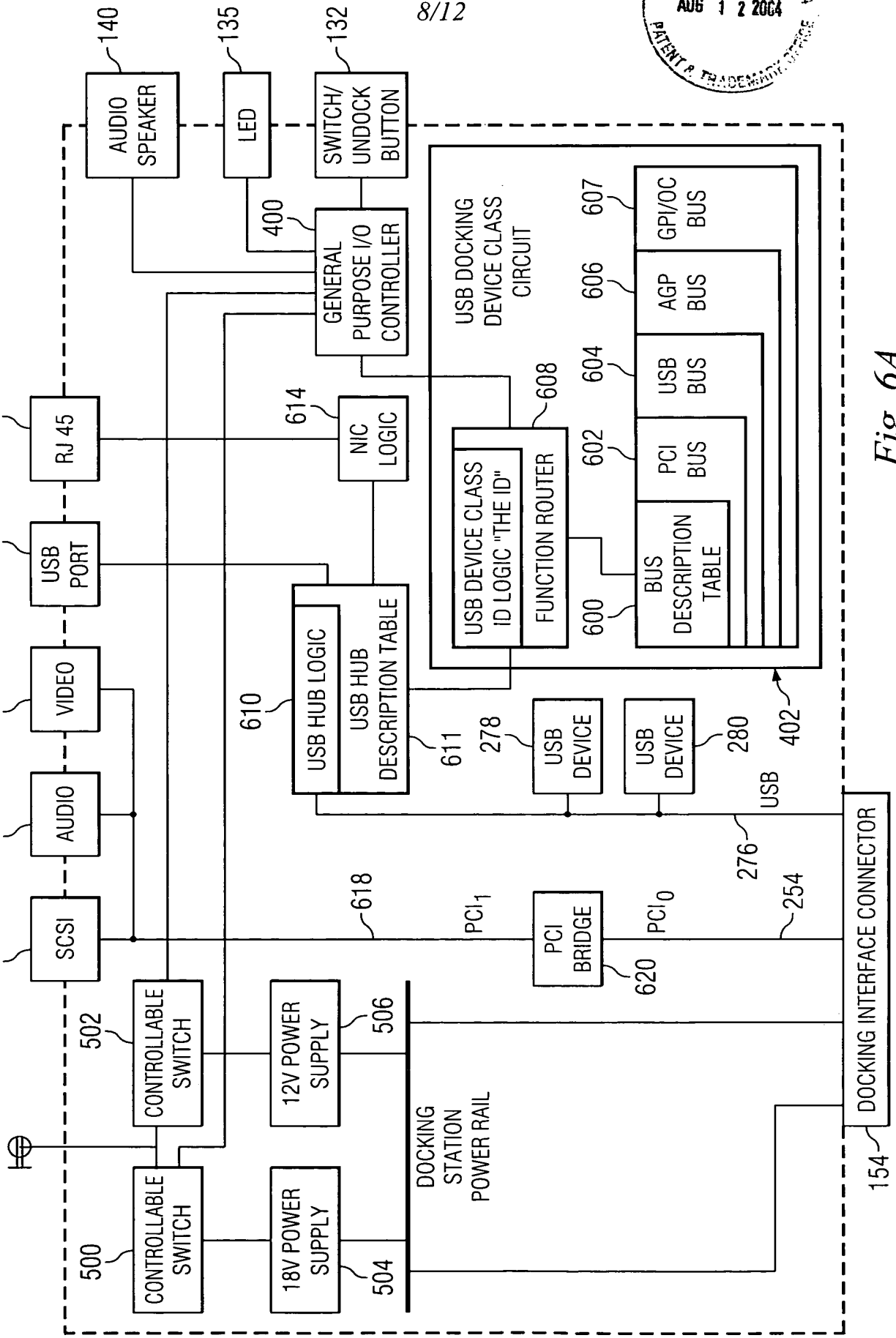

Fig. 6A

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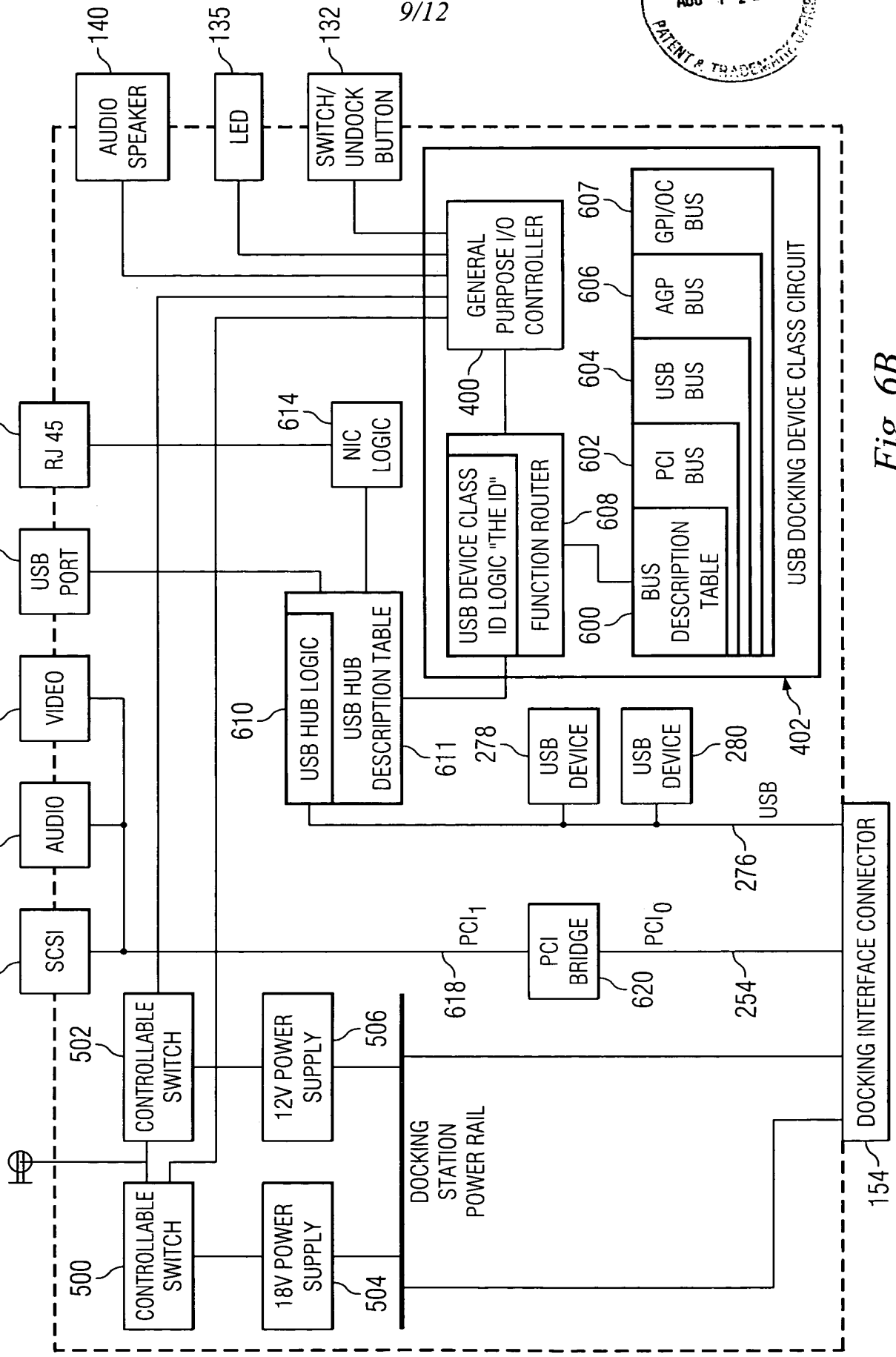

Fig. 6B

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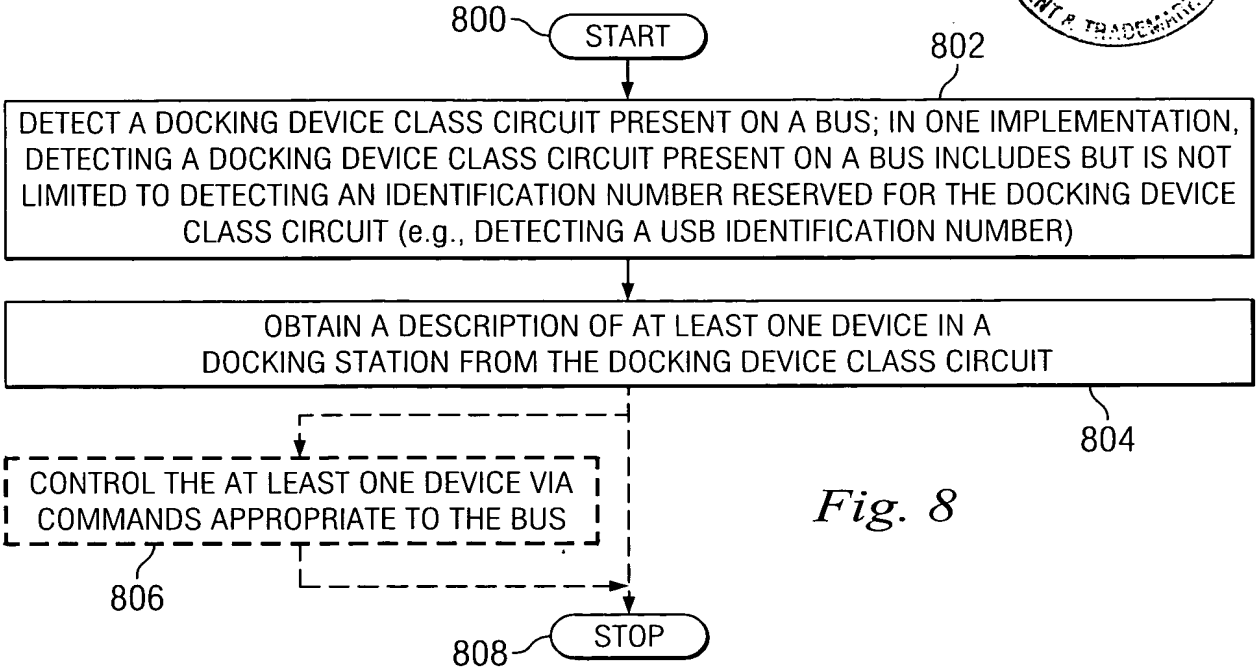

Fig. 8

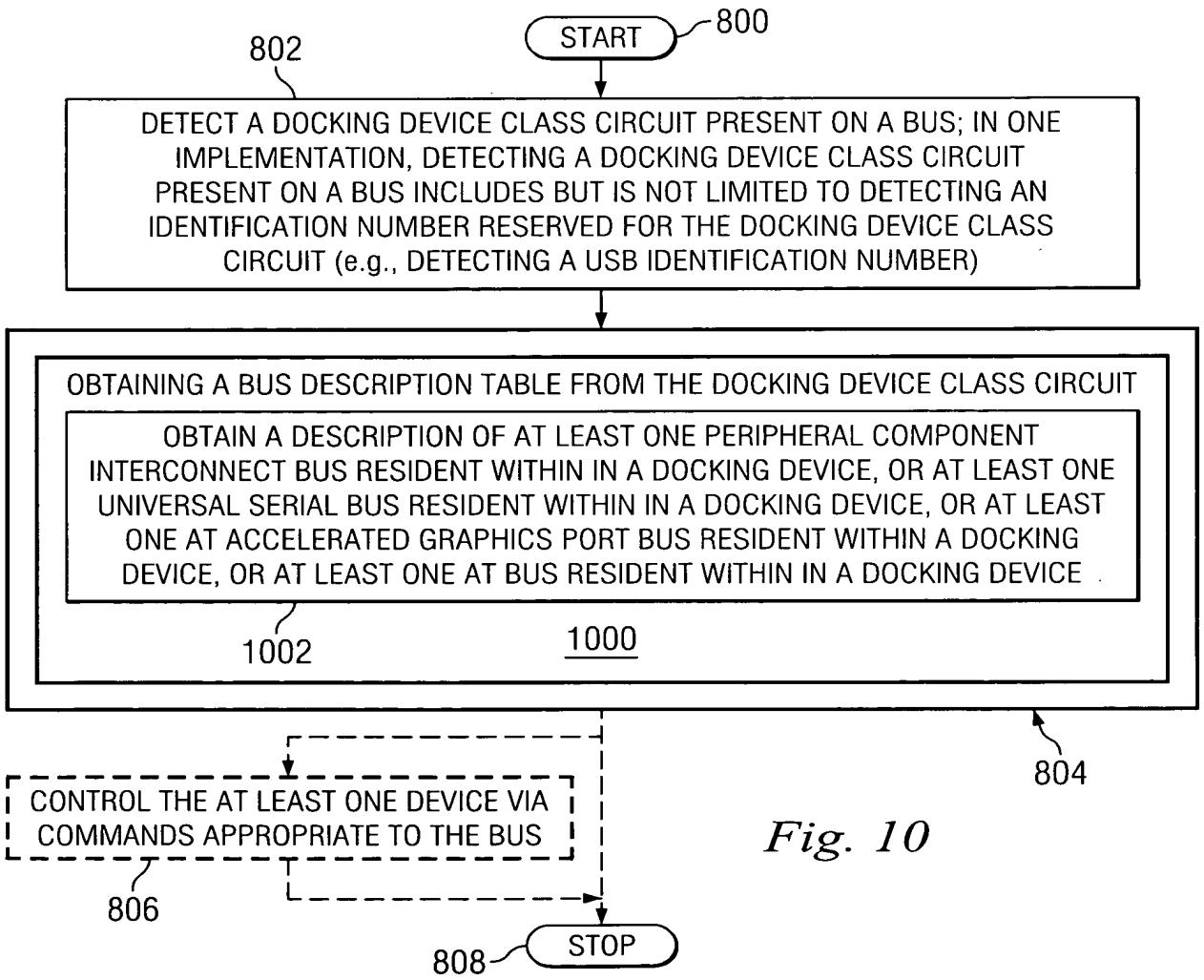

Fig. 10

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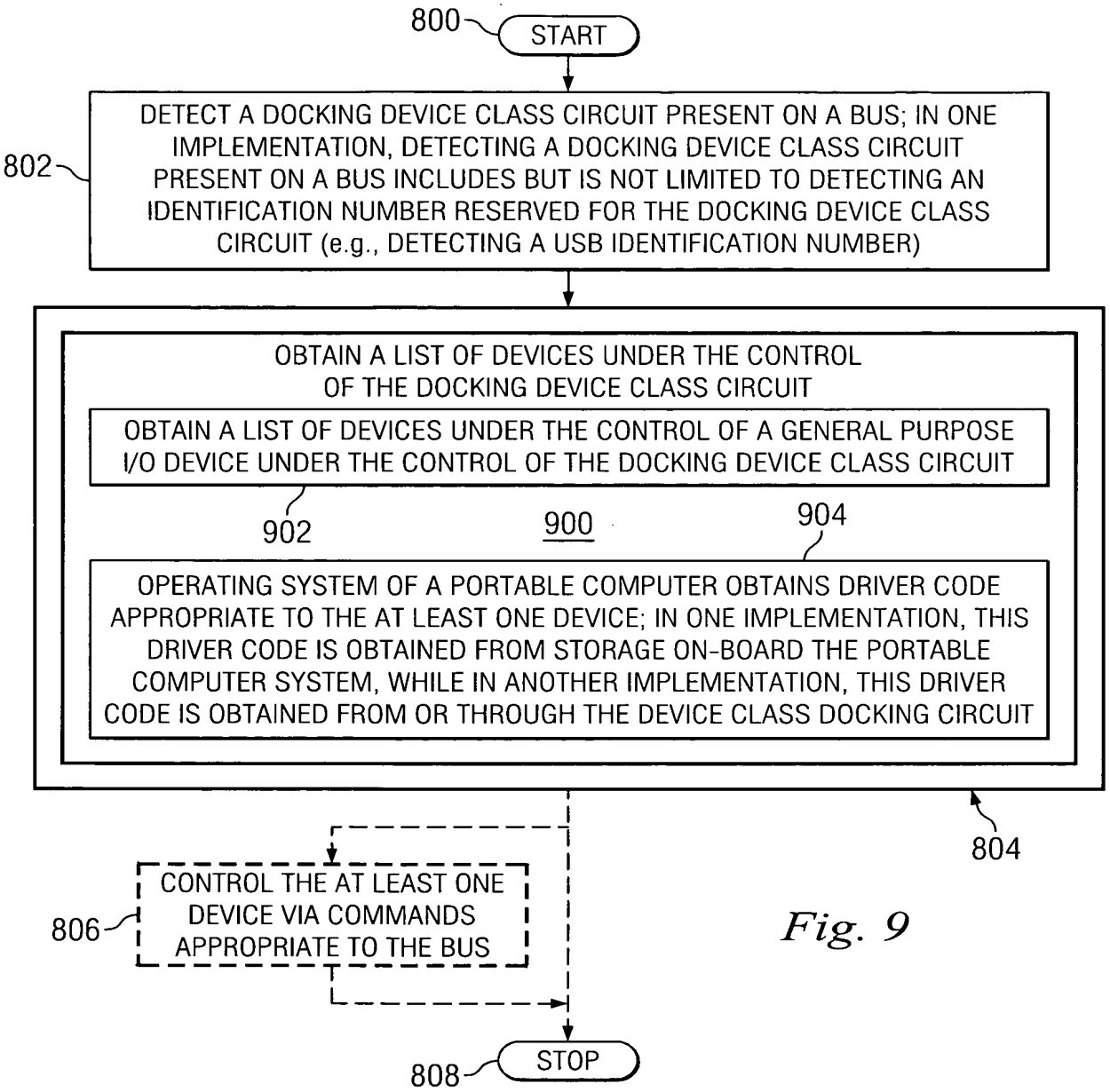

Fig. 9

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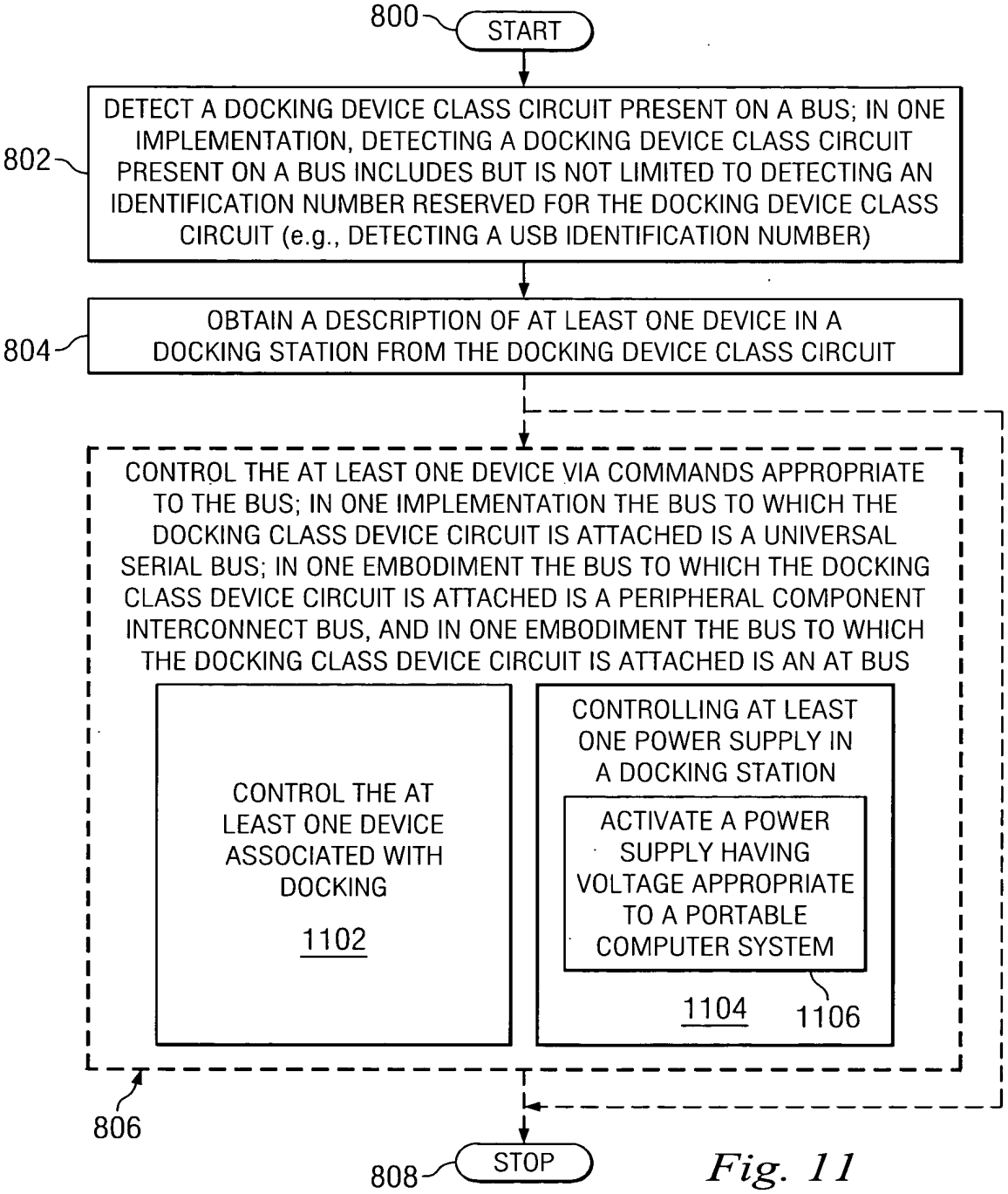

Fig. 11